

09/801,628

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,735,102 B2
DATED : May 11, 2004
INVENTOR(S) : Kech et al.

Page 1 of 6

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,

Line 7, delete "S" and insert therefore -- 5 --.

Line 29, after "the" delete -- is --.

Column 3,

Line 3, delete "S".

Column 6,

Line 25, delete "4 Mbx4" and insert therefore -- 4 Mb x4 --.

Column 8,

Line 6, delete "modest," and insert therefore -- modes, --.

Column 9,

Line 10, delete "vccp" and insert therefore -- Vccp --.

Column 10,

Line 31, delete "dependant" and insert therefore -- dependent --.

Column 12,

Line 14, delete "PMOS" and insert therefore -- pMOS --.

Column 17,

Line 30, delete "show" and insert therefore -- shown --.

Column 18,

Line 7, delete "DVC2Up/Down" and insert therefore -- DVC2 Up/Down --.

Line 61, delete "CMAT" and insert therefore -- CA01 --.

Column 23,

Line 28, delete "mapvccp" and insert therefore -- mapVccp --.

Line 35, delete "the" first occurrence.

Column 24,

Line 19, delete "So" and insert therefore -- 50 --.

Line 48, after "60" delete -- ' --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 25,

Line 15, delete second occurrence "72" and insert therefore -- 72', --.

Line 15, delete "74' Once" and insert therefore -- 74'. Once --.

Line 19, delete "8-16" and insert therefore -- 8x16 --.

Line 22, delete "data lines" and insert therefore -- datalines --.

Line 35, delete "FIG. S" and insert therefore -- FIG. 5 --.

Column 26,

Line 2, delete "data lines" and insert therefore -- datalines --.

Column 27,

Line 18, delete "a".

Line 27, delete "has-a" and insert therefore -- has a --.

Line 37, delete "NMOS" and insert therefore -- nMOS --.

Column 31,

Line 48, delete "DC 21" and insert therefore -- DC21 --.

Column 33,

Line 59, after "33C" delete "5".

Column 34,

Line 3, delete "vcc" and insert therefore -- Vcc --.

Lines 23 and 32, delete "mapvccp" and insert therefore -- mapVccp --.

Column 37,

Lines 3 and 22, delete "PMOS" and insert therefore -- Pmos --.

Line 46, delete "advertized" and insert therefore -- advertised --.

Line 51, delete "dependance" and insert therefore -- dependence --.

Column 40,

Lines 11, delete "vcc" and insert therefore -- Vcc --.

Lines 13 and 34, delete "PMOS" and insert therefore -- pMOS --.

Line 38, delete "that" second occurrence and insert therefore -- the --.

Column 41,

Line 50, delete "additional" and insert therefore -- addition --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 42,

Line 1, delete "PMOS" and insert therefore -- pMOS--.

Line 6, delete "NMOS" and insert therefore -- nMOS --.

Column 43,

Line 28, after "The" delete -- is --.

Line 29, delete "REG 2 VBBON" and insert therefore -- REG2VBBON --.

Lines 42, 44, 45 and 49, delete "vbb" and insert therefore -- vbb --.

Column 44,

Line 56, delete "vccp" and insert therefore -- Vccp --.

Column 45,

Line 28, after "variable" delete -- a --.

Line 32, delete "airing." and insert therefore -- a ring. --.

Line 42, delete "PMOS" and insert therefore -- pMOS --.

Line 42, delete "NMOS" and insert therefore -- nMOS --.

Column 46,

Line 20, after "current" delete -- , --.

Line 64, delete "NMOS" and insert therefore -- nMOS --.

Column 50,

Line 5, delete "from" and insert therefore -- form --.

Line 30, delete "RASD" and insert therefore -- RAS D --.

Column 51,

Line 6, delete "circuit-which" and insert therefore -- circuit which --.

Column 52,

Line 42, delete "p col" and insert thereafter -- pcol --.

Line 62, delete "4 k8 k" and insert thereafter -- 4k8k --.

Line 63, delete "8 k 16 k" and insert thereafter -- 8k 16k --.

Line 66, delete "49 C" and insert thereafter -- 49C --.

Column 54,

Line 16, delete "vccp" and insert thereafter -- Vccp --.

Column 57,

Line 55, delete "900" and insert thereafter -- 90°--.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 59,

Line 25, after "decoders" delete -- , --.

Column 60,

Line 22, after "conjunction" delete "-".

Line 61, delete "DVC2PRIBE" and insert thereafter -- DVC2PROBE --.

Column 61,

Line 33, delete "32 MEGEN" and insert thereafter -- 32MEGEN --.

Lines 33-34, delete "32 MEGEN*." and insert thereafter -- 32MEGEN*. --.

Line 34, delete "32 MEGEN" and insert thereafter -- 32MEGEN --.

Line 43, delete "32 MEGEN*." and insert thereafter -- 32MEGEN*. --.

Line 43, delete "32 MEGEN" and insert thereafter -- 32MEGEN --.

Line 45, delete "32 MEGEN*." and insert thereafter -- 32MEGEN*. --.

Lines 64, 65 delete "32 MEGENa" and insert thereafter -- 32MEGENa --.

Line 65, delete "32 MEGENb" and insert thereafter -- 32MEGENb --.

Column 62,

Line 1, delete "32 MEGENa" and insert thereafter -- 32MEGENa --.

Line 1, delete "32 MEGENb" and insert thereafter -- 32MEGENb --.

Column 63,

Line 20, delete "CA01" and insert thereafter -- CA01 --.

Column 64,

Line 33, delete "G 252" and insert thereafter -- G252 --.

Column 66,

Line 23, after "state" delete -- - --.

Column 67,

Lines 38 and 54, delete "NMOS" and insert therefore -- nMOS --.

Line 55, after "to" delete -- - --.

Column 68,

Line 38, delete "N,AND" and insert therefore -- NAND --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 70.

Line 28, delete "the" first occurrence.

Line 40, delete "is,low," and insert therefore -- is low, --.

Line 65, after "low" delete -- - --.

Column 72.

Line 11, delete "description"and insert therefore -- description --.

Column 75.

Line 53, delete "his" and insert therefore -- This --.

Column 82.

Line 39, delete "various"and insert therefore -- Various --.

Column 83.

Line 10, delete "fanning" and insert therefore -- forming --.

Line 19, delete "army"and insert therefore -- array --.

Lines 33 and 35, delete "pound" and insert therefore -- ground --.

Line 39, delete "round" and insert therefore -- ground --.

Column 84.

Line 2, delete "amid" and insert therefore -- said --.

Line 7, delete "army" and insert therefore -- array --.

Line 41, delete "amid" and insert therefore -- said --.

Line 54, delete "multiplexer" and insert therefore -- multiplexers --.

Line 56, delete "block" and insert therefore -- blocks --.

Line 66, delete "fur" and insert therefore -- for --.

Column 85.

Line 3, after "wherein"delete -- , --.

Line 8, delete "way"and insert therefore -- array --.

Line 16, delete "mono" and insert therefore -- in one --.

Line 26, delete "block," and insert therefore -- blocks, --.

Line 41, delete "may" and insert therefore -- array --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 86,

Line 2, delete "currying" and insert therefore -- carrying --.
Line 19, delete "pound" and insert therefore -- ground --.
Line 27, delete "aback" and insert therefore -- a back --.
Line 47, delete "so" and insert therefore -- to --.
Line 59, delete "end" and insert therefore -- and --.
Line 60, delete "tanning" and insert therefore -- running --.
Line 65, delete "end" and insert therefore -- and --.

Column 87,

Line 4, delete "for" and insert therefore -- form --.
Line 4, delete "may" and insert therefore -- array --.
Line 14, after "blocks" insert -- , --.
Line 16, delete "may" and insert therefore -- array --.
Line 19, delete "pound" and insert therefore -- ground --.
Line 47, delete "multiplexer." and insert therefore -- multiplexers. --.

Column 88,

Line 4, delete "firming" and insert therefore -- running --.

Signed and Sealed this

Twenty-sixth Day of April, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office